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Patent Abstracts of Japan

Cell Stuffing

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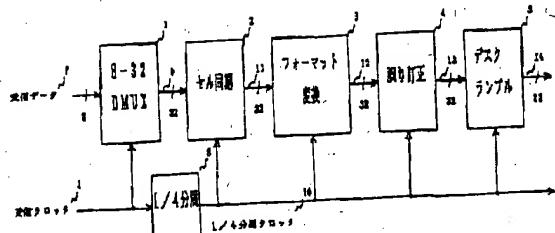
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TITLE : ATM CELL PROCESSOR



ABSTRACT : PURPOSE: To process an ATM cell with a circuit element for a low speed by eliminating one-byte for an HEC area from N-byte parallel input cell data and inserting a stuff byte to the deleted area for each N-cell period.

CONSTITUTION: Eight-parallel ATM cell reception data 7 are subjected to 1:4 demultiplexing by an 8-32 DMUX circuit 1 and the result is fed to a cell synchronization circuit 2. The 32-parallel data 11 whose cell synchronization is established in the synchronization circuit 2 are given to a format conversion circuit 3, which deletes a HEC byte not required after HEC arithmetic operation and inserts a stuff byte to a location from which the HEC byte is deleted to execute format conversion of the 32 parallel data 11 whose cell synchronization is established. The 32 parallel data 11 subjected to format conversion are given to an error correction circuit 4 and a descramble circuit 5, in which the data are processed.

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